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APPLICATION NO.	FILING DATE	FIRST NAMED INVENTOR	ATTORNEY DOCKET NO.	CONFIRMATION NO.
10/045,651	11/07/2001	Amnon Ganot	Q80298	1909

23373 7590 12/13/2006

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EXAMINER

LAVIN, CHRISTOPHER L

ART UNIT PAPER NUMBER

2624

DATE MAILED: 12/13/2006

Please find below and/or attached an Office communication concerning this application or proceeding.

Office Action Summary

Application No.

10/045,651

Applicant(s)

GANOT ET AL.

Examiner

Christopher L. Lavin

Art Unit

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-- The MAILING DATE of this communication appears on the cover sheet with the correspondence address --

Period for Reply

A SHORTENED STATUTORY PERIOD FOR REPLY IS SET TO EXPIRE 3 MONTH(S) OR THIRTY (30) DAYS, WHICHEVER IS LONGER, FROM THE MAILING DATE OF THIS COMMUNICATION.

- Extensions of time may be available under the provisions of 37 CFR 1.136(a). In no event, however, may a reply be timely filed after SIX (6) MONTHS from the mailing date of this communication.
- If NO period for reply is specified above, the maximum statutory period will apply and will expire SIX (6) MONTHS from the mailing date of this communication.
- Failure to reply within the set or extended period for reply will, by statute, cause the application to become ABANDONED (35 U.S.C. § 133). Any reply received by the Office later than three months after the mailing date of this communication, even if timely filed, may reduce any earned patent term adjustment. See 37 CFR 1.704(b).

Status

- 1) ☒ Responsive to communication(s) filed on 27 September 2006.
- 2a) ☐ This action is **FINAL**. 2b) ☒ This action is non-final.
- 3) ☐ Since this application is in condition for allowance except for formal matters, prosecution as to the merits is closed in accordance with the practice under *Ex parte Quayle*, 1935 C.D. 11, 453 O.G. 213.

Disposition of Claims

- 4) ☒ Claim(s) 16 - 18 and 20 - 24 is/are pending in the application.
- 4a) Of the above claim(s) _____ is/are withdrawn from consideration.
- 5) ☐ Claim(s) 17, 18, and 20 - 24 is/are allowed.
- 6) ☒ Claim(s) 16 is/are rejected.
- 7) ☐ Claim(s) _____ is/are objected to.
- 8) ☐ Claim(s) _____ are subject to restriction and/or election requirement.

Application Papers

- 9) ☐ The specification is objected to by the Examiner.
- 10) ☒ The drawing(s) filed on 07 November 2001 is/are: a) ☒ accepted or b) ☐ objected to by the Examiner.
Applicant may not request that any objection to the drawing(s) be held in abeyance. See 37 CFR 1.85(a).
Replacement drawing sheet(s) including the correction is required if the drawing(s) is objected to. See 37 CFR 1.121(d).
- 11) ☐ The oath or declaration is objected to by the Examiner. Note the attached Office Action or form PTO-152.

Priority under 35 U.S.C. § 119

- 12) ☐ Acknowledgment is made of a claim for foreign priority under 35 U.S.C. § 119(a)-(d) or (f).
- a) ☐ All b) ☐ Some * c) ☐ None of:
- ☐ Certified copies of the priority documents have been received.
 - ☐ Certified copies of the priority documents have been received in Application No. _____.
 - ☐ Copies of the certified copies of the priority documents have been received in this National Stage application from the International Bureau (PCT Rule 17.2(a)).

* See the attached detailed Office action for a list of the certified copies not received.

Attachment(s)

- 1) ☐ Notice of References Cited (PTO-892)
- 2) ☐ Notice of Draftsperson's Patent Drawing Review (PTO-948)
- 3) ☐ Information Disclosure Statement(s) (PTO/SB/08)
Paper No(s)/Mail Date _____
- 4) ☒ Interview Summary (PTO-413)
Paper No(s)/Mail Date 09/25/06
- 5) ☐ Notice of Informal Patent Application
- 6) ☐ Other: _____

DETAILED ACTION

Claim Rejections - 35 USC § 102

1. The following is a quotation of the appropriate paragraphs of 35 U.S.C. 102 that form the basis for the rejections under this section made in this Office action:

A person shall be entitled to a patent unless –

(b) the invention was patented or described in a printed publication in this or a foreign country or in public use or on sale in this country, more than one year prior to the date of application for patent in the United States.

2. Claim 16 is rejected under 35 U.S.C. 102(b) as being anticipated by Kosmowski (5,529,441).

In regards to claim 16, Kosmowski discloses A method for recording an image to be recorded by a direct image scanner on an upper layer of a multi-layer printed circuit board substrate in alignment with a pattern on a lower layer thereof, the method comprising: detecting at least two holes provided in the upper layer of a multi-layered printed circuit board substrate that includes said upper layer and at least one lower layer, said at least one lower layer and said at least two holes passing through said upper layer without passing through the lower layer located immediately beneath said upper layer, wherein said at least one lower layer is attached to said upper layer during said detection (Figures 1, 2, 3, 6, and 8: Kosmowski teaches of drilling holes into a multi-layer circuit board. The holes reveal the pattern shown in figure 3, this pattern is used for alignment.); and scanning an electrical circuit pattern on the upper layer in predetermined alignment with said at least two holes (col. 7, lines 38 - 50).

Allowable Subject Matter

1. Claims 17, 18 and 20 – 24 are allowed.

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2. The following is an examiner's statement of reasons for allowance:

The art of record does not teach nor does it suggest the specific features called for in the claims. The claims call for aligning a pattern on an upper layer with respect to at least one hole, which is drilled into the upper layer, but not through the lower layer while the two layers are attached. The concept of aligning a pattern on the upper layer based on holes, either going through the lower layer or not, is not of and by itself new. Combining this concept with the idea of placing the pattern based on a predetermined alignment with the circuitry on the lower board in relationship to the holes overcomes the prior art.

Conclusion

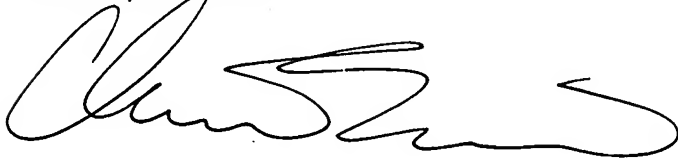

Any inquiry concerning this communication or earlier communications from the examiner should be directed to Christopher L. Lavin whose telephone number is 571-272-7392. The examiner can normally be reached on M - F (8:30 - 5:00).

If attempts to reach the examiner by telephone are unsuccessful, the examiner's supervisor, Bhavesh M. Mehta can be reached on (571) 272-7453. The fax phone number for the organization where this application or proceeding is assigned is 571-273-8300.

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Information regarding the status of an application may be obtained from the Patent Application Information Retrieval (PAIR) system. Status information for published applications may be obtained from either Private PAIR or Public PAIR. Status information for unpublished applications is available through Private PAIR only. For more information about the PAIR system, see <http://pair-direct.uspto.gov>. Should you have questions on access to the Private PAIR system, contact the Electronic Business Center (EBC) at 866-217-9197 (toll-free). If you would like assistance from a USPTO Customer Service Representative or access to the automated information system, call 800-786-9199 (IN USA OR CANADA) or 571-272-1000.

Christopher Lavin

A handwritten signature in black ink, appearing to read 'Chris Lavin', with a long horizontal flourish extending to the right.A handwritten signature in black ink, appearing to read 'Matthew C. Bella', with a stylized, cursive script.

MATTHEW C. BELLA
SUPERVISORY PATENT EXAMINER
TECHNOLOGY CENTER 2600